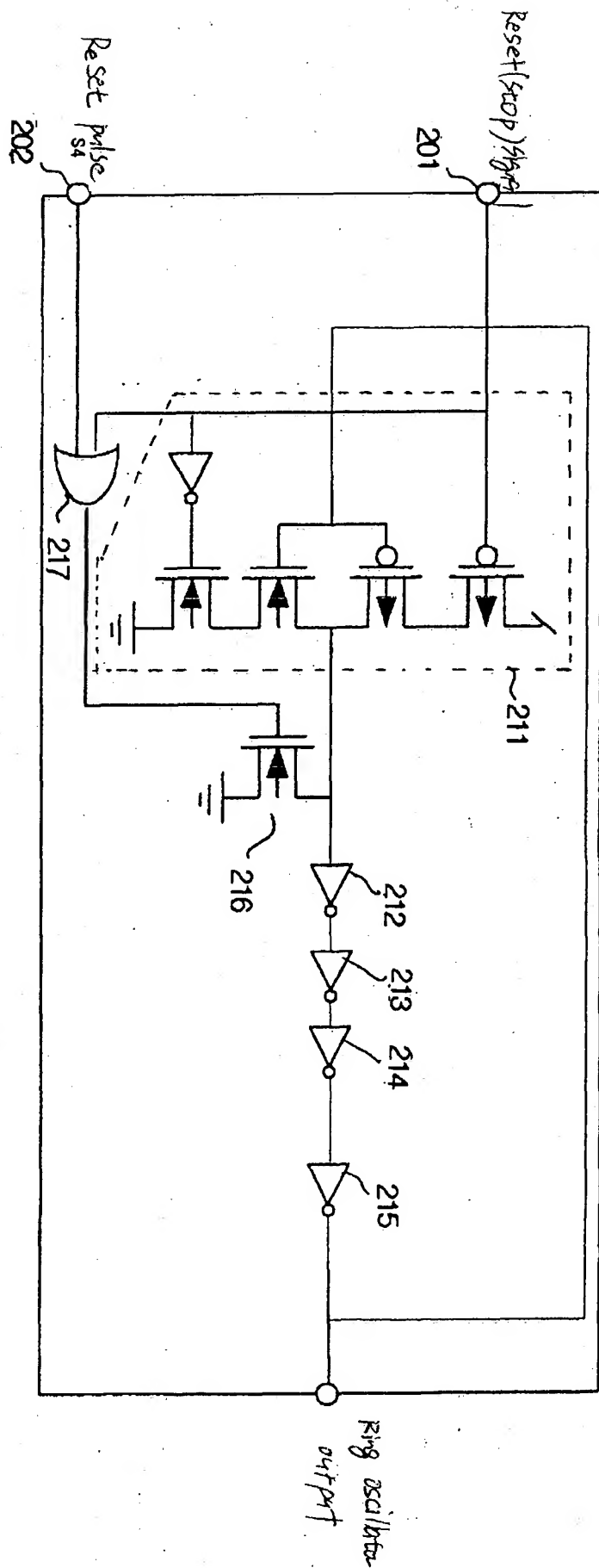
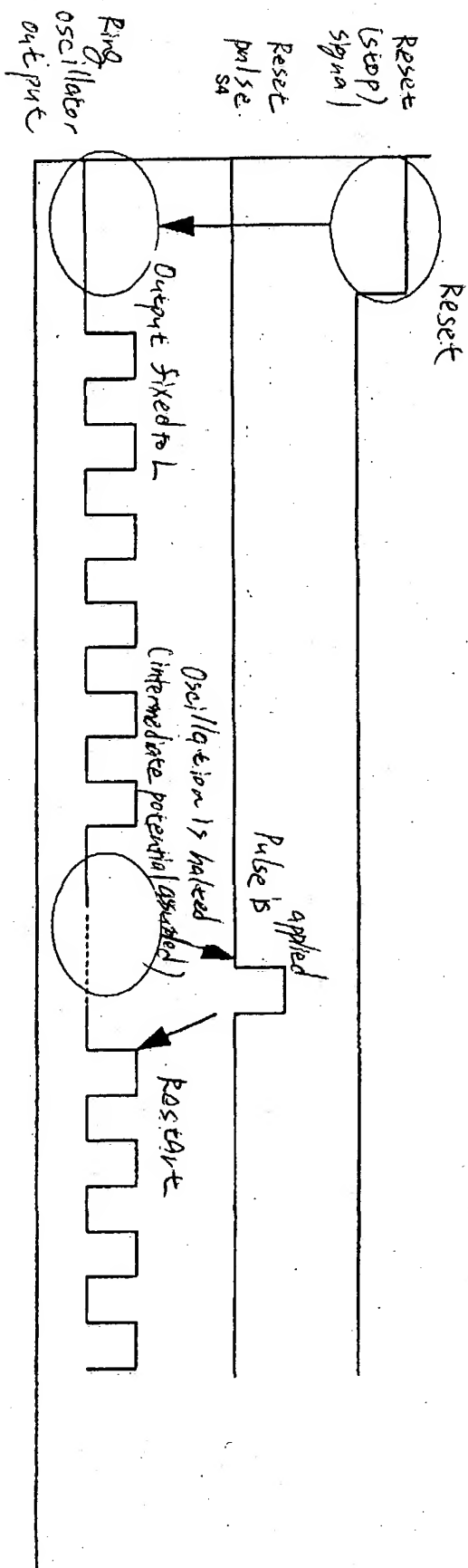


[Fig. 4]

[Fig. 2]



Ring oscillator having an odd number of stages.

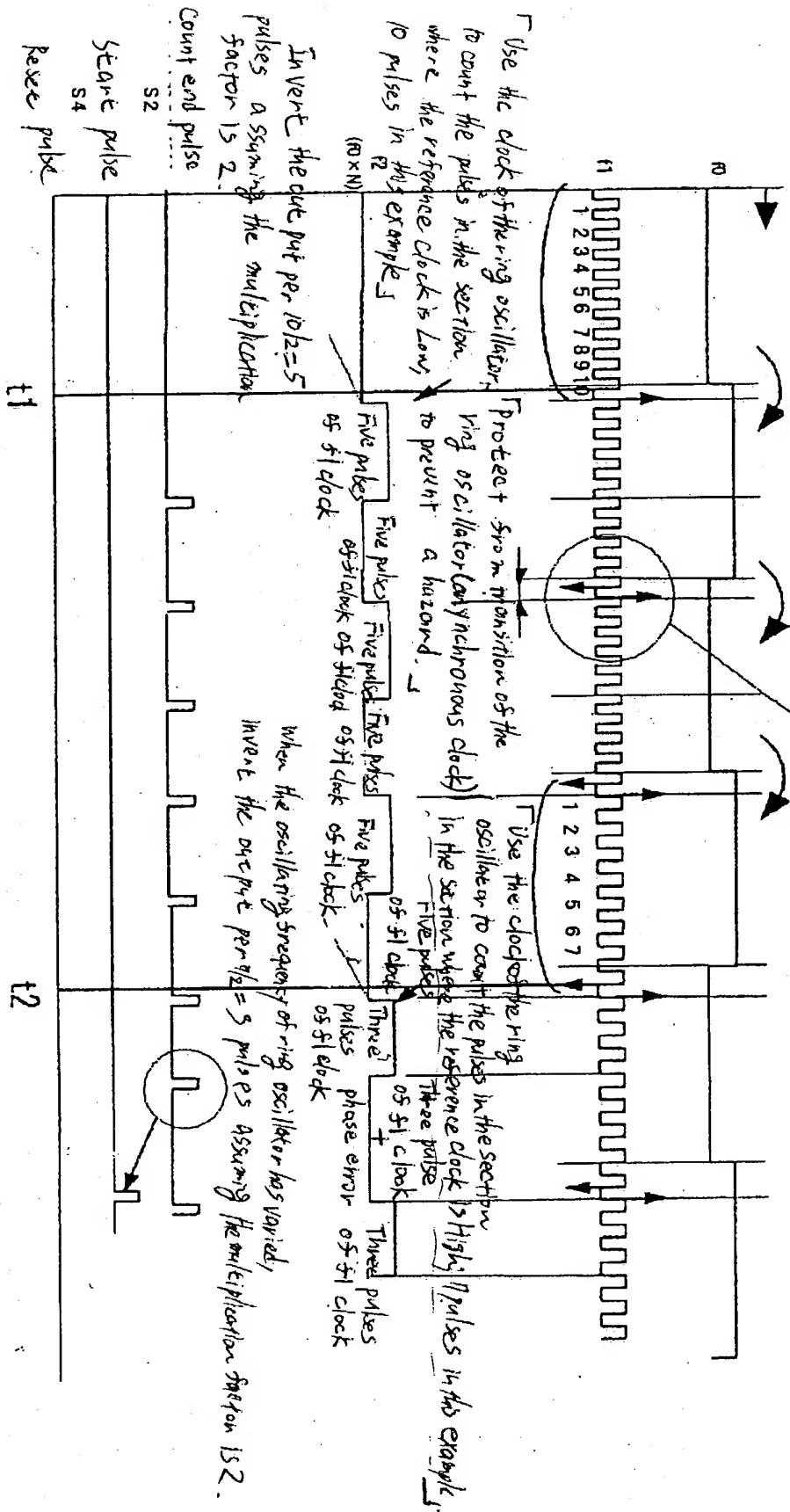


[Fig. 3]

Start (Multiplication factor  $N=2$ )

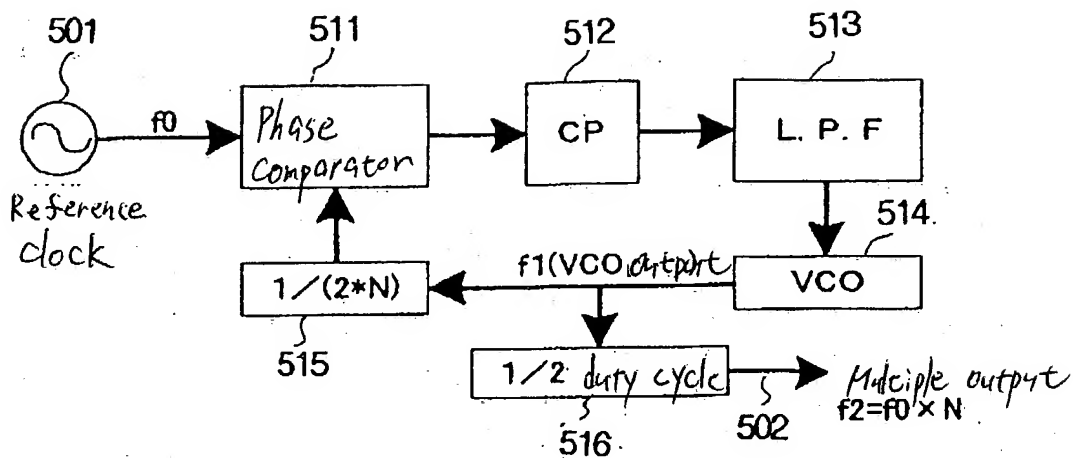
Update a variation in the ring oscillator with a counter at any time

Programmable counter seizes a variation in the reference clock on the trailing edge of the output clock and varies the output on its leading edge.



80  
[J]  
[E]

[Fig. 5]



[Fig. 6]

